

REMARKS

Reconsideration and allowance of the application are respectfully requested in light of the above amendments and the following remarks.

At the outset, the Applicants wish to thank the examiner and his supervisor for the courtesy extended to the Applicants' representative during a personal interview conducted on February 13, 2007. A summary of the substance of the claims, prior art and issues discussed during the interview is included below. The participants in the interview are as set forth in the Examiner's Interview summary of February 13, 2007.

The subject matter of claim 6 has been clarified and integrated into claim 1, and claim 6 has been canceled. Additional support for the amendment of claim 1 is provided in Fig. 3. These amendments were not presented earlier because of the unforeseeability of the remarks presented in the Final Rejection.

Claims 1-3 and 6-8 were rejected, under 35 USC §103(a), as being unpatentable over Bates et al. (US 5,338,625) in view of Rezvani et al. (US 6,633,073). Claims 1-3 and 6-8 in light of Bates et al. and Rezvani et al. were discussed during the interview. The points set forth below were discussed during the interview. To the extent the pending rejections may be deemed

applicable to the amended claims, the Applicants respectfully traverse.

Claim 1 now defines a battery mounted integrated circuit device in which a solid state battery is carried on a second diffusion layer and surrounded by a first diffusion layer such that the second diffusion layer overlaps the first diffusion layer. Consequently, the solid state battery is surrounded by the first diffusion layer and the second diffusion layer. With this configuration, it is possible to further prevent positive ions involved in a charge/discharge of the solid state battery from diffusing into the region where an integrated circuit is mounted. This configuration also allows the integrated circuit to be disposed anywhere in the periphery of the region where the solid state battery is mounted (see, for example, specification page 18, lines 4-15).

As noted in the Final Rejection, Rezvani discloses in Fig. 1B a quiet region 110 that is surrounded by a deep n-well 130 (i.e., second diffusion layer) so as to prevent noise from entering the quiet region. Within a channel of deep n-well 130, Rezvani discloses an n+ ring 140 (i.e., a first diffusion layer, as characterized in the Final Rejection) (see Final Rejection page 3, lines 2-4).

However, as may be determined by inspection of Rezvani's Fig. 1B, Rezvani does not disclose or suggest that quiet region 110 or a solid state battery are carried on deep n-well 130 or on n+ ring 140. Similarly, Bates does not disclose or suggest that a solid state battery is carried on a deep n-well and/or an n+ ring.

Thus, even assuming, arguendo, that Rezvani and Bates suggest isolating a battery from a noisy region 120 by placing the battery in a quiet region 110, as proposed in the Final Rejection, Rezvani and Bates do not disclose or suggest carrying either Rezvani's quiet region 110 or a battery on Rezvani's deep n-well 130 (i.e., second diffusion layer) and/or Rezvani's n+ ring 140 (i.e., first diffusion layer). As a result, Bates and Rezvani do not disclose or suggest the feature now recited in claim 1 of a solid state battery that is carried on a second diffusion layer and surrounded by a first diffusion layer.

Accordingly, Bates and Rezvani, considered individually or in combination, do not render obvious the subject matter now defined by claim 1. Therefore, allowance of claim 1 and all claims dependent therefrom is warranted.

In view of the above, it is submitted that this application is in condition for allowance and a notice to that effect is respectfully solicited.

If any issues remain which may best be resolved through a telephone communication, the Examiner is requested to telephone the undersigned at the local Washington, D.C. telephone number listed below.

Respectfully submitted,


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JEL/DWW/att

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